ABSTRACT

A diagnostic read circuit provides RAM data for external analysis. The RAM or RAMs and diagnostic read circuit are integrated within an integrated circuit. If a data storage error occurs, the diagnostic read circuit provides the RAM data for external analysis. The diagnostic read circuit includes an address generator that generates RAM addresses for data retrieval and a data register that temporarily stores the retrieved data and provides the data to at least one external pin for analysis. The RAM address may also be provided with the data.